

| No | Information of every subject  |                           |
|----|---|---------------------------|
| 1  | Unit name:  | ELECTRICAL MACHINE DESIGN |
| 2  | Code:   | EP 41021                  |
| 3  | Classification:   | Engineering subject       |
| 4  | Credit value:   | 2.5                       |
| 5  | Semester/ Year Offered:   | 1/4                       |
| 6  | Pre-requisite:  |                           |
| 7  | Mode of delivery:   | Lecture, Tutorial         |
| 8  | Assessment system and breakdown of marks:   |                           |
|    | Test  | 20%                       |
|    | Mid-term Examination  | 30%                       |
| 9  | Academic staff teaching unit:   |                           |
| 10 | <p>Course outcome of unit:</p> <p>In this course students will be able</p> <ul style="list-style-type: none"> <li>• To describe the magnetic circuit, electric circuit and design theory involved in the design of electrical machines.</li> <li>• To describe the ampere turns requirement and calculation for the electrical machine</li> <li>• To explain all important aspect of windings of rotating electrical machine</li> <li>• To design the transformers with required specification.</li> </ul>  |                           |
| 11 | <p>Synopsis of unit:</p> <p>EP-4021: The course covers the design the machines. The course introduces four chapters. Chapter 4 is devoted to explain the magnetic circuit calculations involved in the design of electrical machines. A small chapter is include as chapter 5. In chapter 6 all important aspect of windings of rotating electrical machine has been take up. The design of transformer is explained in chapter 7. Complete sample designs of distribution and power transformers are work out. This course is also intended to serve the need of Electrical Engineers in the field of Electrical Machine industries.</p> |                           |

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|  | <p>Topic:</p> <p><b>Chapter      Title</b></p>   |
|  | <p><b>Chapter. 4 Magnetic Circuit Calculations</b></p> <p>4.1 Review of magnetic circuit formulas</p> <p>4.2 Magnetization characteristics</p> <p>4.3 Core loss</p> <p>    4.3.1 Hysteresis loss</p> <p>    4.3.2 Eddy current loss</p> <p>    4.3.3 Total iron loss</p> <p>    4.3.4 Iron loss curve</p> <p>4.4 Permissible flux densities</p> <p>4.5 Estimation of total mmf</p> <p>    4.5.1 Mmf for the air gap</p> <p>    4.5.2 mmf for teeth</p> <p>    4.5.3 Estimation of mmf for tapered teeth</p> <p>    4.5.4 True and apparent tooth densities</p> <p>4.6 Magnetizing current</p> <p>4.7 Magnetic circuit leakage and calculation</p> <p>    4.7.1 Leakage flux</p> <p>    4.7.2 Leakage reactance</p> <p>    4.7.3 Leakage reactance in transformer</p> <p>    4.7.4 Leakage reactance in rotation machine</p> <p><b>Chapter. 5 Electromagnetic</b></p> <p>5.1 Introduction</p> <p>5.2 Magnetic pull or force</p> <p>5.3 The ampere turn requirement</p> <p>5.4 Temperature rise</p> <p><b>Chapter. 6 Electrical Circuits: Armature windings</b></p> <p>6.1 Introduction</p> <p>6.2 Armature type</p> <p>6.3 Winding type</p> <p>6.4 D.C armature winding</p> <p>6.5 A.C armature winding</p> |

- 6.6 The e.m.f equation
- 6.7 Armature reaction
- 6.8 Power loss in conductors

## **Chapter.7 Transformer**

- Introduction
- Transformer type
- Constructional parts
- Core
- Core sections
- Core assembly
- Yoke section
- Windings
- Standard conductors
- The leads
- Bushings
- Cooling
- Tank
- Transformer oil
- Specification
- Output equation
- Staking factor
- Design of core section
- Selection of design constant
- Yoke dimensions
- Over all core dimensions
- Dimension of shall type transformer
- Design of winding
- Choice of winding
- Design of insulation
- Estimation of operating characteristics
- Mechanical stresses
- Effect of frequency variation
- Design of cooling system

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|----|---|
|    | <p>Major design problem</p> <p>Design of welding transformer</p>  |
| 14 | <p><b>Main references:</b></p> <p>EP-31021 &amp; 32021 Electrical Machine and Operation</p>   |
| 15 | <p><b>Additional references:</b></p> <p>Electrical Machine, R.K.RAJPUT, Third Edition</p> <p>Principle of Electric Machine and Power Electronic, P.C.SEN, Second Edition</p> <p>Electric Machine and Electromechanics, SYED A.NASAR, Second Edition</p> |

| No | Course Information  |  |
|----|---|--|
| 1  | Unit name:  | Linear Control System I  |
| 2  | Code:   | EP 41027   |
| 3  | Classification:   | Engineering subject  |
| 4  | Credit value:   | 2.5  |
| 5  | Semester/ Year Offered:   | 1/4  |
| 6  | Pre-requisite:  | 3011(EECA)   |
| 7  | Mode of delivery:   | Lecture, Tutorial  |
| 8  | Assessment system and breakdown of marks:                       |  |
|    | Test  | 20%  |
|    | Mid-term/ final Examination                                     | 30%  |
| 9  | Academic staff teaching unit:                                   |  |
| 10 | Course outcome of unit:<br>In this course students will be able | <ul style="list-style-type: none"> <li>➤ to describe the definitions of the components of the functional block diagram for Control System</li> <li>➤ to apply the loop and node equations with the state and output equations of the electric and mechanical circuit with independent variables</li> <li>➤ to apply state transition matrix (STM) in State-Variable Equations</li> </ul>   |
| 11 | Synopsis of unit:   | The course covers the linear system. The course introduces students to control system, definitions of control system, electric circuit and components, state concepts, transfer function and block diagram, Lagrange's equation, standard input to control systems, transient response: classical method, CAD accuracy checks (CADAC), state-variable equations, evaluating the transition matrix and complete solution of the state equation. |

Topic:

**Chapter**

**Title**

1.

**Introduction**

- 1.1 Introduction
- 1.2 Introduction to Control Systems
- 1.3 Definitions
- 1.4 Historical Background
- 1.5 Digital Control Development
- 1.6 Mathematical Background
- 1.7 The Engineering Control Problem
- 1.8 Computer Literacy
- 1.9 Outline of Text

2

**Writing System Equations**

- 2.1 Introduction
- 2.2 Electric Circuits and Components
- 2.3 State Concepts
- 2.4 Transfer Function and Block Diagram
- 2.5 Mechanical Translation Systems
- 2.6 Analogous Circuits
- 2.7 Mechanical Rotational Systems
- 2.8 Effective Moment of Inertia and Damping of a Gear Train
- 2.9 Thermal Systems
- 2.10 Hydraulic Linear Actuator
- 2.11 Liquid-Level System
- 2.12 Rotating Power Amplifiers
- 2.13 DC Servomotor
- 2.14 AC Servomotor
- 2.15 Lagrange's Equation

3

**Solution of Differential Equations**

- 3.1 Introduction
- 3.2 Standard Inputs to Control Systems
- 3.3 Steady-State Response: Sinusoidal Input

|    |   |
|----|---|
|    | <p>3.4 Steady-State Response: Polynomial Input</p> <p>3.5 Transient Response: Classical Method</p> <p>3.6 Definition of Time Constant</p> <p>3.7 Example: Second-Order System Mechanical</p> <p>3.8 Example: Second-Order System Electrical</p> <p>3.9 Second-Order Transients</p> <p>3.10 Time-Response Specifications</p> <p>3.11 CAD Accuracy Checks (CADAC)</p> <p>3.12 State-Variable Equations</p> <p>3.13 Characteristic Values</p> <p>3.14 Evaluating the State Transition Matrix 1</p> <p>3.15 Complete Solution of the State Equation</p> |
| 14 | <p>Main references:</p> <p>1 D’Azzo, J.J., and C.H. Houppis: Linear control system analysis and design: conventional and modern, 4<sup>th</sup> ed., McGraw- Hill, New York, 1995</p>   |
| 15 | <p>Additional references:</p> <p>Fundamentals of Electric Circuits, 3rd Edition, Alexander and Sadiku</p>   |

| No | Course Information   |                              |
|----|--|------------------------------|
| 1  | Unit name:   | Programmable Logic Control I |
| 2  | Code:  | EP 41028                     |
| 3  | Classification:  | Engineering subject          |
| 4  | Credit value:  | 2.5                          |
| 5  | Semester/ Year Offered:  | 1/4                          |
| 6  | Pre-requisite:   | NA                           |
| 7  | Mode of delivery:  | Lecture, Practical           |
| 8  | Assessment system and breakdown of marks:  |                              |
|    | Test   | 20%                          |
|    | Mid-term Examination   | 30%                          |
| 9  | Academic staff teaching unit:  | 6                            |
| 10 | <p>Course outcome of unit:</p> <p>In this course students will be able</p> <ul style="list-style-type: none"> <li>• To memorize the basic knowledge of PLC, components of PLC hardware</li> <li>• To memorize the useful numbering system and codes for PLC</li> <li>• To draw the relationship between the relay schematics, ladder logic programs and the equivalent logic gate circuits</li> <li>• To memorize the basic instructions of PLC programming and field devices used in PLC connection and process control</li> <li>• To Apply the components of relay schematic diagrams, compare the PLC connections with I/O modules and built the logic gate circuits by using software</li> </ul> |                              |
| 11 | <p>Synopsis of unit:</p> <p>The course covers the fundamental and components of PLC, number systems and logics and input/output control devices commonly found in PLC installations. This course introduces students to PLC, the parts of PLC, input/output control devices commonly found in PLC installations, and fundamental of logics and illustrates the</p>   |                              |



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|  | <p>ladder diagram for the process of the machine operation.</p> <p>Topic:</p> <p><b>Chapter      Title</b></p>   |
|  | <p><b>1. Programmable Logic Controllers(PLCs): An Overview</b></p> <ul style="list-style-type: none"> <li>• Programmable Logic Controllers</li> <li>• Parts of a PLC</li> <li>• Principles of Operation</li> <li>• Modifying the Operation</li> <li>• PLCs versus Computers</li> <li>• PLC size and Application</li> </ul> <p><b>2. PLC Hardware Components</b></p> <ul style="list-style-type: none"> <li>• The I/O Section</li> <li>• Discrete I/O Modules</li> <li>• Analog I/O Modules</li> <li>• Special I/O Modules</li> <li>• I/O Specifications</li> <li>• The Central Processing Unit (CPU)</li> <li>• Memory Design</li> <li>• Memory Types</li> <li>• Programming Terminal Devices</li> <li>• Recording and Retrieving Data</li> <li>• Human Machine Interfaces(HMIs)</li> </ul> <p><b>3. Number Systems and Codes</b></p> <ul style="list-style-type: none"> <li>• Decimal System</li> <li>• Binary System</li> <li>• Negative Numbers</li> <li>• Octal System</li> <li>• Hexadecimal System</li> <li>• Binary Coded Decimal (BCD) System</li> </ul> |

- Gray Code
- ASCII Code
- Parity Bit
- Binary Arithmetic
- Floating Point Arithmetic

#### **4. Fundamentals of Logic**

- The Binary Concept
- AND, OR, and NOT Functions
- Boolean Algebra
- Developing Logic Gate Circuits From Boolean Expressions
- Producing the Boolean Equation for a Give Logic Gate Circuit
- Hardwired Logic Versus Programmed Logic
- Programming Word Level Logic Instructions

#### **5. Basics of PLC Programming**

- Processor Memory Organization
- Program Scan
- PLC Programming Languages
- Bit-Level Logic Instructions
- Instruction Addressing
- Branch Instructions
- Internal Relay Instructions
- Programming Examine If Closed and Examine If Open Instructions
- Entering the Ladder Diagram
- Modes of Operation
- Connecting with Analog Devices

#### **6. Developing fundamental PLC Wiring Diagrams and Ladder Logic Programs**

- Electromagnetic Control Relays
- Contactors
- Motor Starters
- Manually Operated Switches
- Mechanically Operated Switches
- Sensors
- Output Control Devices
- Seal-In Circuits
- Electrical Interlocking Circuits
- Latching Relays
- Converting Relay Schematics into PLC Ladder Program

|    |   |
|----|---|
|    | <ul style="list-style-type: none"><li>• Writing a Ladder Logic Program Directly from a Narrative Description</li><li>• Instrumentation</li></ul>                        |
| 14 | <p><b>Main references:</b></p> <ol style="list-style-type: none"><li>1. FrankD. Petruzella, Fifth Edition.</li><li>2. Previous Editions: 2011, 2005 and 1998.</li></ol> |
| L  | <p><b>Additional references:</b></p>  |

## Information on Lab Practical

| Job | Title  |
|-----|--|
| 1.  | <p><b>Study on equipment of motor control and components of PLC</b></p> <p><b>Objectives:</b></p> <ul style="list-style-type: none"><li>- To study some of typical symbols used in motor control circuit diagrams</li><li>- To state the names of PLC hardware components</li><li>- To represent motor control drawings and ladder diagram of PLC.</li></ul>   |
| 2.  | <p><b>JOB-2 Direct on line Motor Control System</b></p> <p><b>Objectives</b></p> <ul style="list-style-type: none"><li>- To understand the usage of relay logic diagram</li><li>- To make wiring connection for direct on line motor control system</li><li>- To explain the operation of the motor control system.</li></ul> <p><b>Required equipment</b></p> <ol style="list-style-type: none"><li>1. Main and sub feeder isolating circuit breakers</li><li>2. Power and control circuit fuses</li><li>3. Indicating pilot lamps</li><li>4. Pushbuttons (stop , start)</li><li>5. Magnetic Contactor</li><li>6. Overload Relay</li><li>7. 3 phase motor</li><li>8. Cables as required</li></ol> |

| <p>3.</p>                   | <p><b>JOB-3 Logic Circuit Modelling and Simulation with Multisim</b></p> <p><b>Objectives:</b></p> <ul style="list-style-type: none"> <li>- To understand the binary concept and the functions of logic gates</li> <li>- To be able to drive Boolean equations for logic circuits</li> <li>- To use the NI Multisim software</li> </ul> <p><b>Required equipment</b></p> <table border="0"> <thead> <tr> <th></th> <th style="text-align: right;"><b>Qty</b></th> </tr> </thead> <tbody> <tr> <td>1. <math>V_{CC}</math> (5V)</td> <td style="text-align: right;">1</td> </tr> <tr> <td>2. Resistor (1k<math>\Omega</math>)</td> <td style="text-align: right;">1</td> </tr> <tr> <td>3. Indicators (pilot lamps)</td> <td style="text-align: right;">2</td> </tr> <tr> <td>4. Switches (SPST)</td> <td style="text-align: right;">3</td> </tr> <tr> <td>5. Switches (SPDT)</td> <td style="text-align: right;">1</td> </tr> <tr> <td>6. AND gate (7408J)</td> <td style="text-align: right;">2</td> </tr> <tr> <td>7. OR gate (7432N)</td> <td style="text-align: right;">1</td> </tr> <tr> <td>8. NOT gate (7404N)</td> <td style="text-align: right;">1</td> </tr> <tr> <td>9. Output (Lamp 5V_1W)</td> <td style="text-align: right;">1</td> </tr> <tr> <td>10. Logic analyzer (XLAI)</td> <td style="text-align: right;">1</td> </tr> </tbody> </table> |  | <b>Qty</b> | 1. $V_{CC}$ (5V) | 1 | 2. Resistor (1k $\Omega$ ) | 1 | 3. Indicators (pilot lamps) | 2 | 4. Switches (SPST) | 3 | 5. Switches (SPDT) | 1 | 6. AND gate (7408J) | 2 | 7. OR gate (7432N) | 1 | 8. NOT gate (7404N) | 1 | 9. Output (Lamp 5V_1W) | 1 | 10. Logic analyzer (XLAI) | 1 |
|-----------------------------|--|--|------------|------------------|---|----------------------------|---|-----------------------------|---|--------------------|---|--------------------|---|---------------------|---|--------------------|---|---------------------|---|------------------------|---|---------------------------|---|
|                             | <b>Qty</b>   |  |            |                  |   |                            |   |                             |   |                    |   |                    |   |                     |   |                    |   |                     |   |                        |   |                           |   |
| 1. $V_{CC}$ (5V)            | 1  |  |            |                  |   |                            |   |                             |   |                    |   |                    |   |                     |   |                    |   |                     |   |                        |   |                           |   |
| 2. Resistor (1k $\Omega$ )  | 1  |  |            |                  |   |                            |   |                             |   |                    |   |                    |   |                     |   |                    |   |                     |   |                        |   |                           |   |
| 3. Indicators (pilot lamps) | 2  |  |            |                  |   |                            |   |                             |   |                    |   |                    |   |                     |   |                    |   |                     |   |                        |   |                           |   |
| 4. Switches (SPST)          | 3  |  |            |                  |   |                            |   |                             |   |                    |   |                    |   |                     |   |                    |   |                     |   |                        |   |                           |   |
| 5. Switches (SPDT)          | 1  |  |            |                  |   |                            |   |                             |   |                    |   |                    |   |                     |   |                    |   |                     |   |                        |   |                           |   |
| 6. AND gate (7408J)         | 2  |  |            |                  |   |                            |   |                             |   |                    |   |                    |   |                     |   |                    |   |                     |   |                        |   |                           |   |
| 7. OR gate (7432N)          | 1  |  |            |                  |   |                            |   |                             |   |                    |   |                    |   |                     |   |                    |   |                     |   |                        |   |                           |   |
| 8. NOT gate (7404N)         | 1  |  |            |                  |   |                            |   |                             |   |                    |   |                    |   |                     |   |                    |   |                     |   |                        |   |                           |   |
| 9. Output (Lamp 5V_1W)      | 1  |  |            |                  |   |                            |   |                             |   |                    |   |                    |   |                     |   |                    |   |                     |   |                        |   |                           |   |
| 10. Logic analyzer (XLAI)   | 1  |  |            |                  |   |                            |   |                             |   |                    |   |                    |   |                     |   |                    |   |                     |   |                        |   |                           |   |
| <p>4.</p>                   | <p><b>JOB-4 Hardwired Logic versus Programmed Logic</b></p> <p><b>Objectives</b></p> <ul style="list-style-type: none"> <li>- To understand the operation of relay ladder schematics</li> <li>- To implement hardwired logic by using relays and relay ladder schematics</li> <li>- To convert relay ladder schematics to ladder logic program</li> </ul> <p><b>Required equipment</b></p> <ol style="list-style-type: none"> <li>1. Main and sub feeder isolating circuit breakers</li> <li>2. Power and control circuit fuses</li> <li>3. Indicating pilot lamps</li> <li>4. Pushbuttons (stop , start)</li> <li>5. Magnetic Contactor</li> <li>6. Overload Relay</li> <li>7. Cables as required</li> </ol>  |  |            |                  |   |                            |   |                             |   |                    |   |                    |   |                     |   |                    |   |                     |   |                        |   |                           |   |

5. **JOB-5 Forward-Reverse Motor Operation**

**Objectives**

- To understand the usage of relay logic diagram
- To change the direction of a three phase motor rotation
- To learn about the principle of electrical interlocking

**Required equipment**

1. Main and sub feeder isolating circuit breakers
2. Power and control circuit fuses
3. Indicating pilot lamps
4. Pushbuttons
5. Magnetic Contactors
6. Overload Relay
7. 3 phase motor
8. Cables as required

| No | Course Information  |   |
|----|---|---|
| 1  | Unit name:  | Design and Layout of Power System (I)   |
| 2  | Code:   | EP 41036  |
| 3  | Classification:   | Engineering subject   |
| 4  | Credit value:   | 2.5   |
| 5  | Semester/ Year Offered:   | 1/4   |
| 6  | Pre-requisite:  | Generation Transmission & Distribution  |
| 7  | Mode of delivery:   | Lecture, Assignment   |
| 8  | Assessment system and breakdown of marks:   |   |
|    | Test  | 20%   |
|    | Mid-term/ final Examination   | 30%   |
| 9  | Academic staff teaching unit:   |   |
| 10 | Course outcome of unit:<br>In this course students will be able   | <ul style="list-style-type: none"> <li>• To explain the properties of different types of LV, MV and HV power cables, their merits for different applications, cable sizing and loss calculations, useful installation practices and cable management systems</li> <li>• To describe transient disturbances on a transmission system due to lightning strokes and switching operations and determine the proper insulation levels of various components in a power system and their arrangement.</li> <li>• To explain the mechanical design of overhead the transmission lines</li> </ul> |
| 11 | Synopsis of unit:   | <ul style="list-style-type: none"> <li>• The course covers the fundamental relations of underground and overhead transmission system design.</li> </ul>   |
|    | Topic:  |   |
|    | <b>Chapter</b>  | <b>Title</b>  |
|    | <b>12. Cables</b>   |   |
|    | <ul style="list-style-type: none"> <li>• Intriduction</li> <li>• Codes and Standard</li> <li>• Types of Cables and Materials</li> </ul> |   |

- Cable Sizing
- Calculation of Losses in Cables
- Fire Properties of Cables
- Control and Communication Cables
- Cable Management Systems

## **7. Transient Overvoltages and Insulation Coordination**

- Introduction
- Traveling Waves
- Effects of Line Terminations
- Junction of Two Lines
- Junction of Several Lines
- Termination in Capacitance and Inductance
- Bewley Lattice Diagram
- Surge Attenuation and Distortion
- Traveling Waves on Three-Phase Lines
- Lightning and Lightning Surges
- Shielding Failures of Transmission Lines
- Effective Shielding
- Determination of Shielding Failure Rate
- Stroke Current Magnitude
- Shielding Design Methods
- Switching and Switching Surges
- Overvoltage Protection
- Insulation Coordination
- Geomagnetic Disturbances and Their Effects on Power System Operations
- 

## **12. Construction of Overhead Lines**

- Introduction
- Factors Affecting Mechanical Design of Overhead Lines
- Character of Line Route
- Right-of-Way
- Mechanical Loading



|    |   |
|----|---|
|    | <ul style="list-style-type: none"> <li>• Required Clearances</li> <li>• Type of Supporting Structures</li> <li>• Mechanical Calculations</li> <li>• Grade of Construction</li> <li>• Line Conductors</li> <li>• Insulator Types</li> <li>• Joint use By Other Utilities</li> <li>• Conductor Vibration</li> <li>• Conductor Motion Caused by Fault Currents</li> <li>•</li> </ul>   |
| 14 | <p><b>Main references:</b></p> <ol style="list-style-type: none"> <li>1. Electric Power Transmission System Engineering Analysis and Design” by Turan Gonen, 2nd Edition, 2009, CRC Press</li> <li>2. Transmission and Distribution Electrical Engineering” by Dr C. R. Bayliss CEng FIET and B. J. Hardy ACGI CEng FIET, 3rd Edition, 2007, Bayliss, C. R. (Colin R.).</li> <li>3. Dr. H.M. Rai, K.C. Singhal, Bhavana Jain: Power System I (India)</li> </ol> |
| 15 | Additional references:  |



| No | Course Information  |   |
|----|---|---|
| 1  | Unit name:  | Power System Analysis I                 |
| 2  | Code:   | EP 41042                                |
| 3  | Classification:   | Engineering subject                     |
| 4  | Credit value:   | 2.5                                     |
| 5  | Semester/ Year Offered:   | 1/4                                     |
| 6  | Pre-requisite:  | Generation, Transmission & Distribution |
| 7  | Mode of delivery:   | Lecture, Assignment                     |
| 8  | Assessment system and breakdown of marks:   |   |
|    | Test  | 20%                                     |
|    | Mid-term Examination  | 30%                                     |
| 9  | Academic staff teaching unit:   | 3                                       |
| 10 | <p>Course outcome of unit</p> <p>In this course students will be able:</p> <ul style="list-style-type: none"> <li>• to calculate the bus admittance and impedance parameter of network</li> <li>• to solve the network models based on the admittance and impedance representations</li> <li>• to analyze the power flow solution by using various iteration methods</li> </ul> |   |
| 11 | <p>Synopsis of unit:</p> <p>The course covers the analysis of power system. This course is discussed the bus admittance and impedance matrix by the building algorithms is formulated and employed for the systematic computation of bus voltages and angles, the comprehensive coverage of the power flow solution of interconnected power system during normal condition.</p> |   |

|    |   |
|----|---|
| 12 | <p>Topic:</p> <p><b>Chapter      Title</b></p>  |
|    | <p><b>7.The Admittance Model and Network Calculations</b></p> <ul style="list-style-type: none"> <li>-Branch and Node Admittances</li> <li>-Mutually Coupled Branches in <math>Y_{bus}</math></li> <li>- An Equivalent Admittance Network</li> <li>-Modification of <math>Y_{bus}</math></li> <li>-The Network Incidence Matrix and <math>Y_{bus}</math></li> <li>-The Method of Successive Elimination</li> <li>-Node Elimination (Kron Reduction)</li> <li>-Triangular Factorization</li> </ul> <p><b>8.The Impedance Model and Network Calculations</b></p> <ul style="list-style-type: none"> <li>-The Bus Admittance and Impedance Matrices</li> <li>- Thevenin's Theorem and <math>Z_{bus}</math></li> <li>-Modification of an Existing <math>Z_{bus}</math></li> <li>-Direct Determination of <math>Z_{bus}</math></li> <li>-Calculation of <math>Z_{bus}</math> Elements from <math>Y_{bus}</math></li> <li>- Mutually Coupled Branches in <math>Z_{bus}</math></li> </ul> <p><b>9. Power- Flow Solutions</b></p> <ul style="list-style-type: none"> <li>-The Power-Flow Problem</li> <li>-The Gauss-Seidel Method</li> <li>- The Newton-Raphson Method</li> <li>-The Newton-Raphson Power-Flow Solution</li> <li>-Regulating Transformers</li> <li>-The Decoupled Power-Flow Method</li> </ul> |
| 13 | <p>Main references:</p> <p>Power System Analysis, John J. Grainger, William D. Stevenson, Jr.</p>   |
| 14 | <p>Additional references:</p> <p>Power System Analysis, Hadi Saadat</p>   |



| No | Course Information  |   |
|----|---|---|
| 1  | Unit name:  | Electrical Machine and Operation  |
| 2  | Code:   | EP-41043  |
| 3  | Classification:   | Engineering subject   |
| 4  | Credit value:   | 2.5   |
| 5  | Semester/ Year Offered:   | 1/3   |
| 6  | Pre-requisite:  | EP-21021& 22021 Electromechanics  |
| 7  | Mode of delivery:   | Lecture, Practical  |
| 8  | Assessment system and breakdown of marks:                       |   |
|    | Test  | 20%   |
|    | Mid-term Examination  | 30%   |
| 9  | Academic staff teaching unit:                                   |   |
| 10 | Course outcome of unit:<br>In this course students will be able | <ul style="list-style-type: none"> <li>• To explain the characteristics of the D.C shunt generator and the function of D.C motor</li> <li>• To calculate the problems of D.C shunt generator and D.C motor</li> <li>• To compute the problems of D.C machine by using various control methods</li> <li>• To identify losses and calculate the efficiency of the D.C machine</li> <li>• To apply the tools for measuring the speed of DC machines</li> </ul> |
| 11 | Synopsis of unit:   | The course covers Electrical Machine and Operation. The course introduces students to direct current generator characteristic, direct current motor, speed control of DC motor, loss efficiency and testing of DC machine.  |

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| 12 | <p>Topic:</p> <p><b>Chapter      Title</b></p> <p><b>1. Direct Current Generator Characteristics</b></p> <ul style="list-style-type: none"> <li>-Characteristics of DC generators</li> <li>-Separately excited generator</li> <li>-Building up the voltage of self-excited shunt generator</li> <li>-Shunt generator characteristics</li> <li>-Series generator</li> <li>-Compound wound generator</li> <li>-Applications of DC generator</li> </ul> <p><b>2. Direct Current Motor</b></p> <ul style="list-style-type: none"> <li>-General aspects</li> <li>-Principle of operation of DC motor</li> <li>-Back or counter E.M.F</li> <li>-Comparison between motor and generator action</li> <li>-Torque developed in motor</li> <li>-Mechanical power developed by motor armature</li> <li>-Types of DC motor</li> <li>-Speed of a DC motor</li> <li>-Speed regulation</li> <li>-Armature reaction and commutation</li> <li>-Motor characteristics</li> <li>-Comparison of DC motor characteristics</li> <li>-Summary of characteristics and applications of DC motors</li> <li>-DC motor reversing</li> <li>-Starting DC motors</li> <li>-Self-governing properties of DC motor</li> </ul> <p><b>3.Speed Control of DC Motors</b></p> <ul style="list-style-type: none"> <li>-Factors controlling the speed</li> <li>-Field control method</li> <li>- Rheostatic control</li> </ul> |

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|    | <ul style="list-style-type: none"> <li>-Voltage control</li> <li>-Thyristor control of DC motor</li> <li>- Electric braking</li> </ul> <p><b>4. Losses, Efficiency and Testing of DC Machines</b></p> <ul style="list-style-type: none"> <li>-Losses and efficiency</li> <li>- Testing of DC machines</li> </ul> |
| 14 | <p><b>Main references:</b></p> <p>ELECTRICAL MACHINES 2<sup>nd</sup> Edition ; R.K. Rajput</p>   |
| 15 | <p>Additional references:</p> <p><a href="http://mysite.du.edu">http://mysite.du.edu</a>&gt; tech&gt;elmotors</p> <p><a href="http://www.explainthatstuff.com">http://www.explainthatstuff.com</a>&gt;how-regener....</p>  |

Approved by-

Prepared by-



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